

PIC18F1XK50/PIC18LF1XK50

Device	Program Memory		Data Memory		I/O ⁽¹⁾	10-bit A/D (ch) ⁽²⁾	ECCP (PWM)	MSSP		EUSART	Comp.	Timers 8/16-bit	USB
	Flash (bytes)	# Single-Word Instructions	SRAM (bytes)	EEPROM (bytes)				SPI	Master I ² C™				
PIC18F13K50/ PIC18LF13K50	8K	4096	512 ⁽³⁾	256	15	11	1	Y	Y	1	2	1/3	Y
PIC18F14K50/ PIC18LF14K50	16K	8192	768 ⁽³⁾	256	15	11	1	Y	Y	1	2	1/3	Y

- Note 1:** One pin is input only.
Note 2: Channel count includes internal Fixed Voltage Reference (FVR) and Programmable Voltage Reference (CVREF) channels.
Note 3: Includes the dual port RAM used by the USB module which is shared with the data memory.

Pin Diagrams

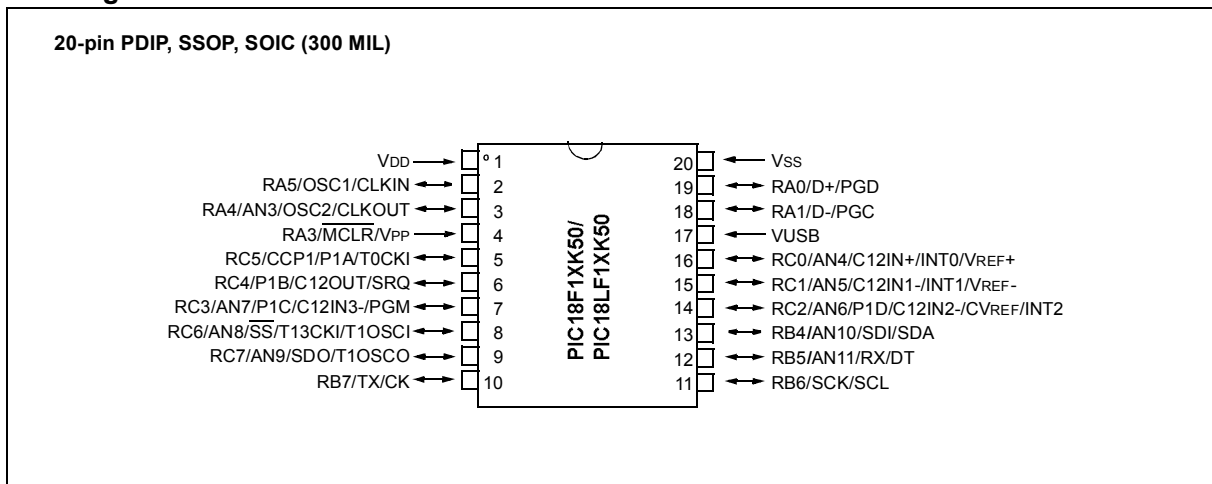


TABLE 1: PIC18F1XK50/PIC18LF1XK50 PIN SUMMARY

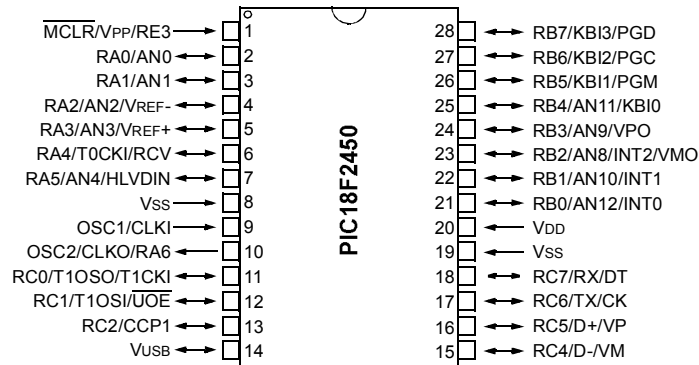
Pin	I/O	Analog	Comparator	Reference	ECCP	EUSART	MSSP	Timers	Interrupts	Pull-up	USB	Basic
19	RA0								IOC		D+	PGD
18	RA1								IOC		D-	PGC
4	RA3 ⁽¹⁾								IOC	Y		MCLR/VPP
3	RA4	AN3							IOC	Y		OSC2/CLKOUT
2	RA5								IOC	Y		OSC1/CLKIN
13	RB4	AN10					SDI/SDA		IOC	Y		
12	RB5	AN11					RX/DT		IOC	Y		
11	RB6						SCL/SCK		IOC	Y		
10	RB7						TX/CK		IOC	Y		
16	RC0	AN4	C12IN+	VREF+					INT0			
15	RC1	AN5	C12IN1-	VREF-					INT1			
14	RC2	AN6	C12IN2-	CVREF	P1D				INT2			
7	RC3	AN7	C12IN3-		P1C							PGM
6	RC4		C12OUT		P1B							SRQ
5	RC5				CCP1/P1A			T0CKI				
8	RC6	AN8					SS	T13CKI/T1OSCI				
9	RC7	AN9					SDO	T1OSCO				
17											VUSB	
1												VDD
20												VSS

- Note 1:** Input only

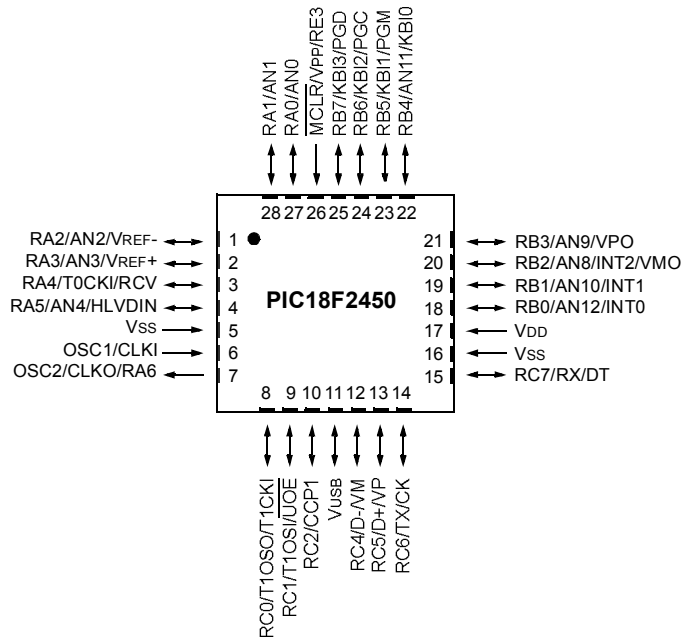
PIC18F2450/4450

Pin Diagrams

28-Pin SPDIP, SOIC



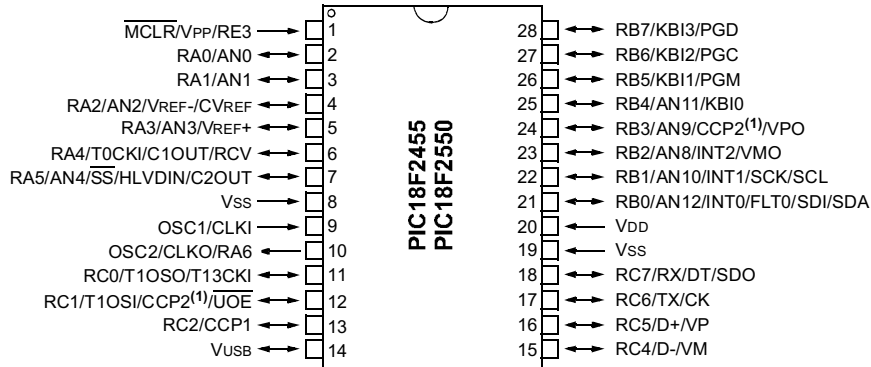
28-Pin QFN



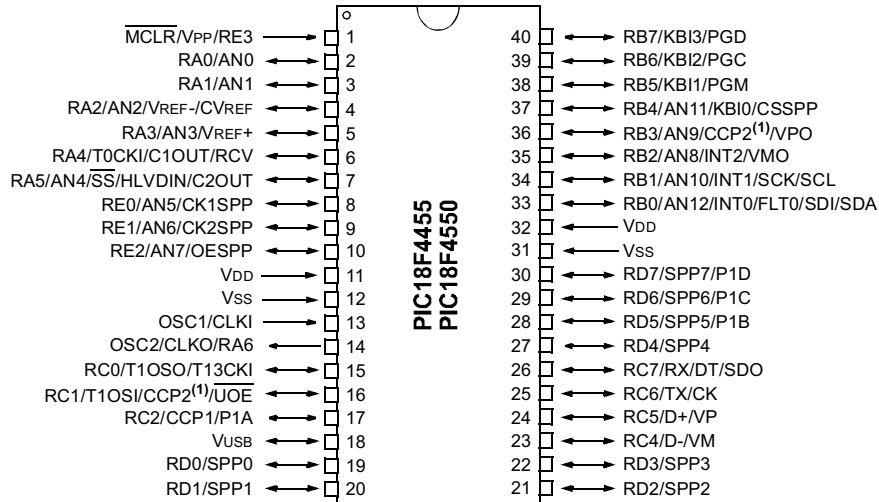
PIC18F2455/2550/4455/4550

Pin Diagrams

28-Pin PDIP, SOIC



40-Pin PDIP

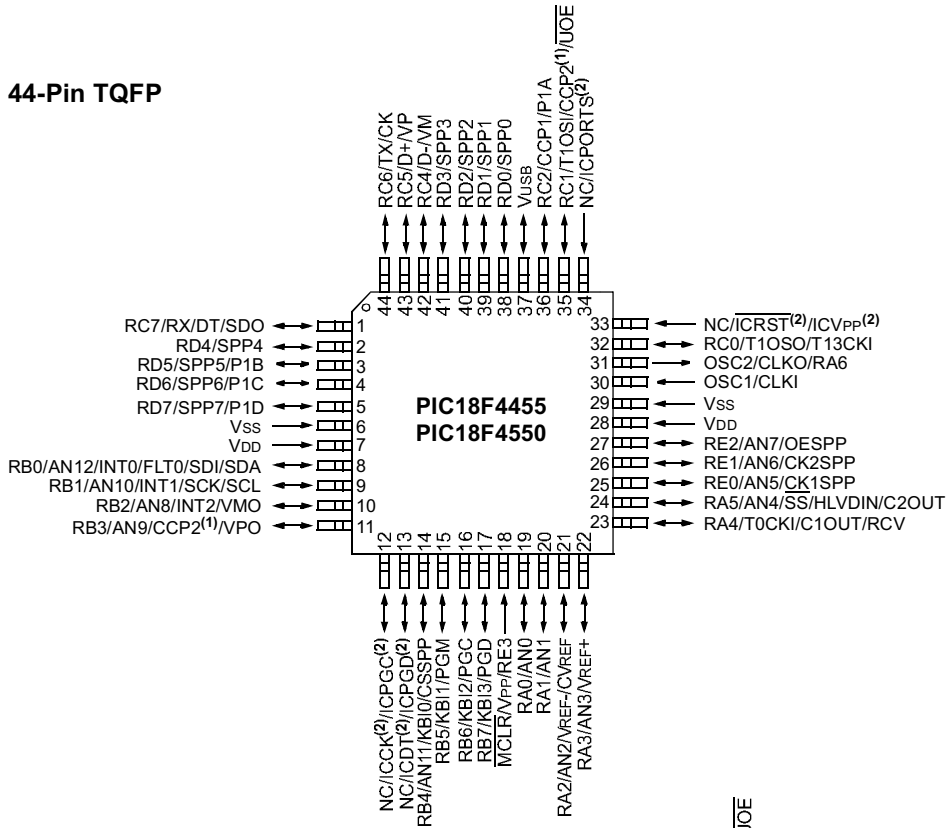


Note 1: RB3 is the alternate pin for CCP2 multiplexing.

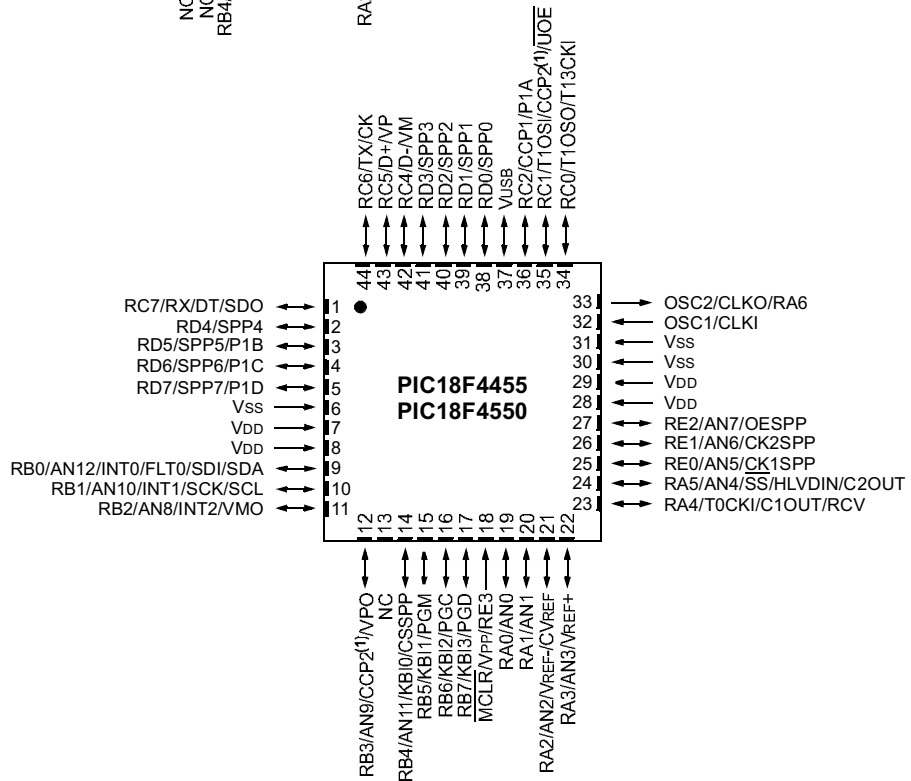
PIC18F2455/2550/4455/4550

Pin Diagrams (Continued)

44-Pin TQFP



44-Pin QFN



Note 1: RB3 is the alternate pin for CCP2 multiplexing.

Note 2: Special ICPORTS features available in select circumstances. See Section 25.9 "Special ICPORT Features (Designated Packages Only)" for more information.